PATENT APPLICATION 09/910,589

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

1

In re Application of:

William A. Huffman, et al.

RECEIVED

Serial No.:

09/910,589

Filing Date:

July 20, 2001

OCT 0 4 2004

Group Art Unit:

2188

Technology Center 2100

Examiner:

Reginald G. Bragdon

Title:

SYSTEM AND METHOD FOR HANDLING

UPDATES TO MEMORY IN A DISTRIBUTED

SHARED MEMORY SYSTEM

Commissioner of Patents P. O. Box 1450 Alexandria, VA 22313-1450

Dear Sir:

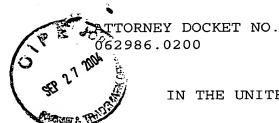
CERTIFICATE OF MAILING BY EXPRESS MAIL

hereby certify that the Information Disclosure Statement with Form PTO-1449 are being deposited with the United States Postal Service "Express Mail Post Office to Addressee" service under 37 C.F.R. § 1.10 on this 27th day of September 2004, addressed to the Commissioner of Patents, Alexandria, VA 22313-1450.

Willie Jiles

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Alexandria, VA 22313-1450

Dear Sir:

INFORMATION DISCLOSURE STATEMENT

Applicant respectfully requests, pursuant to 37 C.F.R. §§1.56, 1.97, and 1.98, that the documents listed on the attached Form PTO-1449 be considered and cited in the examination of the above-identified application. A copy of these documents are enclosed for the convenience of the Examiner. Furthermore, pursuant to 37 C.F.R. §§1.97(g) and (h), Applicant makes no representation that these documents qualify as prior art or that these documents are material to patentability of the present application or that a search has been made.

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REMARKS

An Office Action has been received by the Applicant in the above identified patent application. Pursuant to 37 C.F.R. §1.97(c) enclosed herewith is a check for \$180.00. Please charge any fees or credit any overpayment to Deposit Account No. 02-0384 of BAKER BOTTS L.L.P.

Respectfully submitted,
BAKER BOTTS L.L.P.

Attorneys for Applicant

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Reg. No. 35,870

September 27, 2004

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Customer Number: 05073

Page 1 of 1

PTO-1449

Information Disclosure Citation In an Application

Application	No.
09/910,589	

Applicant(s)

William A. Huffman, et al.

Docket Number 062986.0200

Group Art Unit 2188

Filing Date 7/20/2001

U.S. PATENT DOCUMENTS

	DOCUMENT NO.	DATE	NAME	CLASS	SUBCLASS	FILING DATE
A	5,537,569	07/16/96	Masubuchi			
В	5,606,686	2/25/97	Tarui, et al.	R	ECEIVED	
C	6,073,211	06/06/00	Cheng, et al.	0	CT 0 4 2004	
D	6,192,452	02/20/01	Bannister, et al.	Techn	ology Center 2	100
Е	6,279,084	08/21/01	VanDoren, et al.			
F	6,338,122	01/08/02	Baumgartner, et al.			
G	6,631,447	10/07/03	Morioka, et al.			
H						
I						
J						
K						
L						
M						
N						

FOREIGN PATENT DOCUMENTS

		DOCUMENT NO.	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
							YES	NO
	0							
	P							-
	Q							

	DOCUMENT (Including Author, Title, Source, and Pertinent Pages)	DATE
R	Culler, et al. "Parallel Computer Architecture", 1999 Morgan Kaufmann Publishers, 2 cover pages, pp.553-589.	1999
S		
T		
U		

DATE CONSIDERED

EXAMINER

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP § 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to the applicant.

U.S. PATENT AND TRADEMARK OFFICE